NOTICE OF RECRUITMENT

ARRIVE TECHNOLOGIES VIET NAM CO.,LTD

Work Place:	Ho Chi Minh City
Company Profile:	**Arrive Technologies Viet Nam** http://www.arrivetechnologies.com 10th Floor, Etown Building (1), 364 Cong Hoa, Tan Binh Dist., HCMC, Viet Nam

Arrive Technologies, Inc., with its head quarter in California, is a broadband semiconductor solutions company with a broad portfolio of highly integrated systems-on-a-chip products combining voice, data, Internet and multimedia content for worldwide telecommunications companies. Our ASICs and CodeChips™ provide flexible next-generation solutions allowing our customers to meet their growing broadband connection needs.

Arrive Technologies Viet Nam is a subsidiary of Arrive Technologies Inc., located at Etown Building (1), mainly responsible for researching and designing the Arrive's products.

For the company's expansion, we need to recruit **ten (10) more Logic Design Engineers.**

Company size: **(Quantity of employees)**	100-499
Job Title:	**Logic Design Engineers**
Job Category:	Electrical/Electronics IT/Hardware Telecommunications
Job Description:	-Define, architect & design RTL for ASIC/FPGA/CPLD with Verilog HDL (further details will be discussed in the interview) **Working conditions: - To work at E.town Building, PCs with strong configuration, modern lab (telecoms equipments and embedded systems in MAN/WAN networks, etc...) - Offered competitive salary rate - Dinner & parking allowance at e.town since probation time - Other allowances: transportation, lunch, health insurance at international hospitals located in Viet Nam (such as Phap Viet, Colombia, An Sinh,..)

Job Requirement:

- Bachelor of engineering, major in Telecommunication-Electronics.

- Interested in chip design/studying & programming hardware.

- Ability to self-study & work under high pressure..
- Good knowledge of digital technologies.
-Deep knowledge about telecom system and data transmission.
- Advantage for whom with experience in using such tools as Altera Quartus, Xilinx ISE or Perl, System Verilog languages.
- Ability to read & understand telecommunication standards/books or write reports in English.

Interested candidates please send the resume (describing education, experiences, and expected salary) with subject is the position you would like to apply "Logic Design Engineer" via email or directly to the company address: Arrive Technologies Viet Nam, 10th Fl, e.town Building (1), 364 Cong Hoa, Tan Binh Dist., HCM City before **Apr 30, 2017 (this deadline may be shortened if we get enough CV).

Preferred Language:	English
Qualification:	Bachelor
Experience:	0+ years in chip design
Salary:	Very competitive
Job Type:	Full time
Contact Person:	Ms Thuan
Email for applications:	jobs@atvn.com.vn
District:	Tan Binh
Adress:	10 Fl., Etown Building (1), 364 Cong Hoa
City:	Ho Chi Minh City
Country:	Vietnam
Telephone Number:	08 38127479
Fax Number:	08 38121484
Company Website:	www. arrivetechnologies.com